

# Neil McGlohon, Ph.D.

---

CONTACT	neil@mcglo.dev	www.mcglo.dev github.com/nmcglohon
EDUCATION	<b>Ph.D., Computer Science</b> Rensselaer Polytechnic Institute, Troy, New York USA Research Focus: High Performance/Parallel Computing	<b>July 2021</b>
	<b>M.S., Computer Science</b> Rensselaer Polytechnic Institute, Troy, New York USA Research Focus: Distributed Computing, Machine Learning	<b>May 2016</b>
	<b>Bachelor of Science in Physics</b> University of Oklahoma, Norman, Oklahoma USA Minor: Computer Science	<b>May 2014</b>
RELEVANT SKILLS	<ul style="list-style-type: none"><li>• Self-motivated, fast-learner, quick to ramp-up on new topics</li><li>• Strong and flexible critical-thinking, problem solving, and analysis skills</li><li>• Comfortable in parallel, distributed, or clustered computing environments</li></ul>	<ul style="list-style-type: none"><li>• Languages and Technologies: C/C++, Python, Java, MPI, Git, Linux, Scripting, MATLAB, SLURM, Jupyter, GDB, Tensorflow/PyTorch, WandB, Docker, AWS, L<sup>A</sup>T<sub>E</sub>X</li></ul>
FEATURED PROFESSIONAL EXPERIENCE	<ul style="list-style-type: none"><li>• Maintainer of open-source C/C++ repository for massively parallel simulation of high performance computing (HPC) communication networks used by researchers from institutions including Argonne National Laboratory. Rigorously reviews pull requests to ensure stability prior to merging.</li><li>• Successfully defended dissertation studying the effects of congestion on HPC communication networks and methods of mitigation including adaptive routing, QoS traffic class isolation, and targeted congestion abatement techniques.</li><li>• Mentored graduate students on their research and academic careers. HPC and networking domain expert resource and mentor for undergraduate and graduate students in projects associated with an NSF funded cyber team program spanning five states.</li><li>• Published 15 peer-reviewed papers in various conferences and journals in areas of High Performance Computing, Parallel Discrete Event Simulation, Neuromorphic Computing, and Machine Learning. Led many of the associated research projects, orchestrating experiment design, data acquisition and analysis. Presented papers and their findings at conferences internationally. Full list on last page.</li></ul>	
EMPLOYMENT EXPERIENCE	<b>IBM</b> San Jose, California USA <b>Research Scientist</b> Research and Development of optimized code for custom hardware to implement artificial neural network layers and development of debugging tools to assist validation of implemented architectures.	<b>October 2022 - Present</b>
	<b>Rensselaer Polytechnic Institute</b> , Center for Computational Innovations Troy, New York USA <b>Research Scientist</b> Senior personnel of center; performs complex and leading-edge scientific research and development. Main responsibilities include software development for simulation of high performance composable system technologies for facilitation of next-generation AI hardware platforms. External member of the IBM AI Hardware Center.	<b>July 2021 - September 2022</b>

Spearheads research to compare interconnection network designs and configurations for the facilitation of distributed AI communication workloads through simulation.  
 Designs and implements procedures for benchmarking of AI accelerated supercomputing hardware to guide future acquisitions and demonstrate capability of existing systems.  
 Triage and prioritizes supercomputing center needs for research facilitation of thousands of users.  
 Identifies and pursues research collaboration opportunities with corporate and government partners in coordination with center director.

***Software Engineer*****February 2020 - July 2021**

Software development and research relating to the field of high performance computing. Worked with external research partners in a collaborative environment to advance the state of the art. Primary maintainer for grant sponsored open-source high performance computing network interconnect simulation framework. Participated as guest lecturer on the topic of the Message Passing Interface (MPI) to Parallel Computing course at RPI.

**Cisco Meraki**, San Francisco, California USA

***Software Engineering Intern*****May - August 2017**

Research and development of a thread-safe and lockless read/write HashTable using Read-Copy-Update (RCU) techniques. Submitted to open-source repository for the Software Defined Router: Click. Experience with using JIRA, Gerrit, and associated development tools.

SCIENTIFIC  
SERVICE

2020-Present – NSF CAREERS Cyberteam Program

Steering Committee Member

2022 – ACM SIGSIM Principles of Advanced Discrete Simulation (PADS)

Organizing Committee, Ph.D. Colloquium Co-Chair

2022 – ACM SIGPLAN Benchmarking in the Data Center: Expanding to the Cloud (BID)

Program Committee Member

2022 – IEEE IPDPS Workshop on Scalable Deep Learning over Parallel and Distributed Infrastructure (ScaDL)

Program Committee Member

2019-Present – Peer-reviewer for 6 ACM/IEEE computing conferences and journals.

ACADEMIC  
EXPERIENCE

**Rensselaer Polytechnic Institute**, Troy, New York USA

***Graduate Research Assistant*****August 2014 - July 2021**

M.S. and Ph.D. research, graduate level coursework and projects. Primary area of research: High-Performance/Parallel Computing. Other areas of interest: parallel and distributed systems, cloud computing, machine learning, neuromorphic computing and simulation. Contributor to ROSS parallel discrete event simulation framework.

***Teaching Assistant: Computer Science I*****August - December 2014**

Support to professor during course teaching fundamentals of computer science using Python. Worked in group of eight teaching assistants. Duties included facilitating two lab discussion sections a week, hosting office hours, grading homework and exams, and monitoring/responding to question on the course online forum.

**University of Oklahoma**, Norman, Oklahoma USA

***Undergraduate Research Assistant*****May 2012 - May 2014**

Continued REU research, performing a closer inspection of electrical transport properties of antimony measured at cryogenic temperatures. Worked on developing and refining a method for measuring differential conductance of a material – allowing for a greater understanding of the interface between a topological insulator and a superconductor. Advisor: Dr. Sheena Murphy.

***National Science Foundation REU*****May - August 2012, 2013**

Participated in condensed matter research and analysis under an NSF Materials Research Science and Engineering Center (MRSEC) grant from advisor: Dr. Sheena Murphy.